



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : G09G 3/32	A1	(11) International Publication Number: WO 99/42983 (43) International Publication Date: 26 August 1999 (26.08.99)
(21) International Application Number: PCT/GB99/00383		
(22) International Filing Date: 5 February 1999 (05.02.99)		
(30) Priority Data: 9803441.6 18 February 1998 (18.02.98) GB		
(71) Applicants (<i>for all designated States except US</i>): CAMBRIDGE DISPLAY TECHNOLOGY LTD. [GB/GB]; 181a Huntingdon Road, Cambridge CB3 0DJ (GB). SEIKO EPSON CORPORATION [JP/JP]; 4-1, Nishishinjuku-ku 2-chome, Shinjuku-ku, Tokyo 163-0811 (JP).		(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, GM, HR, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).
(72) Inventors; and		
(75) Inventors/Applicants (<i>for US only</i>): FRIEND, Richard, Henry [GB/GB]; 37 Barton Road, Cambridge CB3 9LG (GB). BURROUGHES, Jeremy, Henley [GB/GB]; 36 Rustat Road, Cambridge CB1 3QT (GB). KIMURA, Mutsumi [JP/JP]; Seiko Epson Corporation, 3-3-5 Owa, Suwa-shi, Nagano-ken 392 (JP). HEEKS, Stephen, Karl [GB/GB]; 127 Rampton Road, Cottenham, Cambridge CB4 4JJ (GB).		
(74) Agents: SLINGSBY, Philip, Roy et al.; Page White & Farrer, 54 Doughty Street, London WC1N 2LS (GB).		
(54) Title: ELECTROLUMINESCENT DEVICES		
(57) Abstract		
An organic light-emitting device having: an organic light-emitting region comprising a plurality of organic light-emitting pixels; switch means each associated with a respective pixel for switching power to that pixel; and drive means for driving each switch means to cycle between a first, low power mode and a second, high power mode, at a frequency sufficient to cause light emission from the associated pixel to appear substantially continuous, the duration of the high power mode relative to the low power mode being variable so as to vary the average brightness of the pixel.		<p>The diagram illustrates a cross-sectional view of an organic light-emitting device. It features a grid of pixels arranged in four columns and four rows. The columns are labeled 13a, 13b, 13c, and 13d at the top, and 14a, 14b, 14c, and 14d below them. The rows are labeled 15a, 15b, 15c, and 15d on the right, and 19a, 19b, 19c, and 19d on the left. The pixels are represented by shaded rectangular areas. Between the columns of pixels are vertical lines representing switch means, and between the rows are horizontal lines representing drive means. At the bottom, there are two small square components labeled 10 and 12. An arrow labeled 16 points to one of the drive lines. Reference numerals 11a through 11d are also present on the left side.</p>

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece			TR	Turkey
BG	Bulgaria	HU	Hungary	ML	Mali	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MN	Mongolia	UA	Ukraine
BR	Brazil	IL	Israel	MR	Mauritania	UG	Uganda
BY	Belarus	IS	Iceland	MW	Malawi	US	United States of America
CA	Canada	IT	Italy	MX	Mexico	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NE	Niger	VN	Viet Nam
CG	Congo	KE	Kenya	NL	Netherlands	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NO	Norway	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	NZ	New Zealand		
CM	Cameroon			PL	Poland		
CN	China	KR	Republic of Korea	PT	Portugal		
CU	Cuba	KZ	Kazakhstan	RO	Romania		
CZ	Czech Republic	LC	Saint Lucia	RU	Russian Federation		
DE	Germany	LI	Liechtenstein	SD	Sudan		
DK	Denmark	LK	Sri Lanka	SE	Sweden		
EE	Estonia	LR	Liberia	SG	Singapore		

ELECTROLUMINESCENT DEVICES

This invention relates to electroluminescent devices, especially ones that use an organic material for light emission and have thin-film transistors for switch circuitry.

One type of electroluminescent device is described in PCT/WO90/13148, the contents of which are incorporated herein by reference. The basic structure of this device is a light-emitting polymer film (for instance a film of a poly(p-phenylenevinylene) - "PPV") sandwiched between two electrodes, one of which injects electrons and the other of which injects holes. The electrons and holes excite the polymer film, emitting photons. These devices have potential as flat panel displays.

Another type of organic light-emitting device is a small molecule device, details of which are given in US 4,539,507, the contents of which are incorporated herein by reference. These have a light-emitting layer which comprises at least one small molecule material such as tris(8-hydroxyquinoline)aluminium ("Alq₃") sandwiched between the two electrodes.

In an organic light-emitting device the organic light-emitting layer is generally divided into individual pixels, which can be switched between emitting and non-emitting states by altering the current flow through them. The pixels are generally arranged in orthogonal rows and columns. Two alternative arrangements for controlling the pixels are generally used: passive matrix and active matrix. In a passive matrix device one of the electrodes is patterned in rows and the other in columns. Each pixel can be caused to emit light by applying an appropriate voltage between the row and column electrodes at whose intersection it lies. In an active matrix display circuitry is provided so that each pixel can be left in an emitting state whilst another pixel is addressed.

Figure 1 illustrates a circuit for driving one pixel in a thin-film transistor ("TFT") active matrix display. The circuit comprises the pixel itself, illustrated as diode 1, which is connected between electrodes 2 and 3. Electrodes 2 and 3 are connected to all the

pixels of the device and a voltage sufficient for emission from the pixel is applied constantly between the electrodes 2 and 3. At least part of the switch circuit 4, which in practice is embodied by thin-film transistors, lies between electrode 3 and the pixel 1. The switch circuit is controlled by way of row and column electrodes 5,6. To cause the pixel 1 to emit light, voltages are applied to the electrode 6, to switch the switching transistor 7 on, and to electrode 5 to charge the storage capacitor 8. Electrode 6 is then turned off. Since the capacitor 8 is charged the current transistor 9 is switched on and the voltage applied at electrode 3 is applied to the pixel, causing it to emit. Although it requires a more complex circuit than a passive matrix device this arrangement has the advantage that the pixel can be held in an emitting state by means of the capacitor 8 whilst other pixels on different rows and columns are addressed by their row and column electrodes. Using the whole backplane area for thin film transistors and metal lines is well known in transparent and reflective LCD displays.

To allow clearer images to be displayed it is important to be able to control the brightness of each pixel individually, so as to provide a grey-scale. In the active matrix device of figure 1 this is done by selecting the voltage applied to electrode 5 and the duration of the pulse applied to electrode 6 so as to fix the charge applied to capacitor 8. The charge on capacitor 8 determines the state of transistor 9 and therefore the current flow to the pixel from electrode 3. The current flow through the pixel determines the brightness of its emission. Figure 2 shows a graph of current through transistor 9 (I) against gate voltage of transistor 9 (V). There is a flat "off" zone 100 where the current and voltage are low and no light is emitted by pixel 1, a sloping transition zone 110 providing intermediate levels of brightness from the pixel 1 and a flat "on" zone 120 where the transistor is in its fully on state and the pixel is fully on. By fixing the charge on capacitor 8 so that the transistor 9 is at the desired point in the transition zone a desired intermediate level of brightness for the pixel can be achieved.

The shape of the curve in figure 2 is determined by the characteristics of the circuit elements, especially current transistor 9. A switch circuit 4 must be provided for every pixel of the display. To achieve the required miniaturisation and low cost the circuit is integrated with the display. However, this arrangement generally leads to

great variability of performance between the current transistors of each pixel of the display. Although the current flow in the off and on zones is fairly consistent between current transistors (because the off-current is almost zero and the on-current is largely determined by the resistance of the diode 1), the current transistors' threshold voltages can differ greatly. This is a particular problem where the light-emitting material of the display is an organic light-emitting material because the amount of light emitted from organic light-emitting pixels is sensitively dependent on the current through the device. Therefore, for the same input line current different organic light-emitting pixels can produce widely differing intermediate brightnesses. This limits the use of this drive scheme for grey-scale organic light-emitting display devices.

According to a first aspect of the present invention there is provided an organic light-emitting device having: an organic light-emitting region comprising a plurality of organic light-emitting pixels; switch means each associated with a respective pixel for switching power to that pixel; and drive means for driving each switch means to cycle between a first, low power mode and a second, high power mode, at a frequency sufficient to cause light emission from the associated pixel to appear substantially continuous, the duration of the high power mode relative to the low power mode being variable so as to vary the average brightness of the pixel.

Each switch means suitably comprises at least one transistor. That transistor is preferably controlled by a gate voltage to switch power through the transistor and to the pixel. In the first mode the transistor preferably blocks current flow to the associated pixel so that pixel is substantially non-emissive. In the second mode the transistor preferably allows current flow to the associated pixel so that the pixel is substantially fully emissive. In the second mode the transistor is preferably in its fully on state. In the first mode the transistor is preferably in its fully off state.

Preferably in one or both of the first and second modes the brightness of the pixel is substantially insensitive to the transistor's characteristics, for example the gate voltage – so that small changes in the gate voltage do not substantially affect the through current to the pixel. The transistor is preferably a thin-film transistor.

The switch means preferably comprises a charge storage means (e.g. a capacitor), suitably connected to the gate of the transistor described above, for holding the transistor in the first mode or the second mode. The charge storage means is suitably charged by means of a second transistor, preferably a thin-film transistor. The switch means is preferably a thin-film transistor switch means.

The display is preferably an active matrix display, most preferably a TFT active matrix display.

The frequency of switching between the first and second modes is suitably greater than 30Hz, preferably greater than 50Hz and most preferably around 60Hz or greater.

The drive means is preferably controllable to vary the duration of the high power mode relative to the low power mode. The duty cycle of the drive scheme (as measured by the duration of the second mode as a proportion of the total cycle time) may vary from zero (when the pixel is off) to 100% (maximum brightness).

In each cycle the high power mode may be provided as one or more than one discrete high power pulse.

Each pixel may suitably comprise at least two independent light-emitting areas, with a switch means associated with each one of the light-emitting areas for independently switching power to that light-emitting area under the control of the drive means.

The organic light-emitting device may comprise: an organic light-emitting region comprising a plurality of organic light-emitting pixels, each pixel comprising at least two independent light-emitting areas; a switch arrangement associated with each pixel and comprising switch means each associated with a respective light-emitting area of that pixel for switching power to that area; and a control means for addressing each pixel by its associated switch arrangement and controlling the brightness of each pixel by selectively driving one or more of the corresponding

switch means to cause selected ones of the light-emitting areas of that pixel to emit light.

Where each pixel is divided into separate light-emitting areas the light-emitting areas of each pixel are suitably of different sizes, preferably of different areas. Preferably, in each pixel, every light-emitting area (except the smallest) is twice (or substantially twice) the size of the next-largest light-emitting area of that pixel.

The drive means is preferably capable of driving each pixel or each light-emitting area to operate non-instantaneously at an intermediate voltage, to allow further control over the brightness of the pixel. Therefore, the drive means is suitably capable of driving the switch means to a non-instantaneous partially on state. The partially on state may be a third, intermediate power mode. Other intermediate power modes may also be provided.

Preferred thicknesses for the pixels and/or light-emitting region are in the range from 20 to 200nm and most preferably around 100nm.

The organic light-emitting pixels and/or areas are suitably formed of a light-emitting polymer material, and preferably of a conjugated material. A suitable material is a semiconductive conjugated polymer such as PPV or a derivative thereof. The light-emitting material of which the pixels and/or light-emitting areas are formed suitably is or comprises PPV, poly(2-methoxy-5(2'-ethyl)hexyloxyphenylene-vinylene) ("MEH-PPV"), a PPV-derivative (e.g. a di-alkoxy or di-alkyl derivative), a polyfluorene and/or a co-polymer incorporating polyfluorene segments, PPVs and/or related co-polymers. It could be deposited by spin-coating, dip-coating, blade-coating, meniscus-coating, self-assembly etc. The constituent of the light-emitting region and/or its precursor may be water-based: examples are precursor-based PPVs. An alternative material is an organic molecular light-emitting material, e.g. Alq₃, or any other small sublimed molecule or conjugated polymer electroluminescent material as known in the prior art.

The organic light-emitting pixels and/or areas are suitably deposited by ink-jet printing.

To improve the performance of the device a conductive polymer layer may be provided adjacent to the light-emitting region. The conductive polymer layer preferably comprises polyethylene dioxythiophene ("PEDT"), polystyrene sulphonic acid doped polyethylene dioxythiophene ("PEDT-PSS"), doped polyaniline, a doped alkylthiophene and/or a doped polypyrrole. The thickness of the layer is suitably less than 200nm, preferably less than 100nm and most preferably less than or around 50nm. The sheet resistance of the layer is suitably greater than 10^6 or 10^7 Ohms/square, preferably greater than 10^8 Ohms/square and most preferably greater than or around 10^{10} Ohms/square.

The present invention will now be described by way of example with reference to the accompanying drawings, in which:

figure 3 shows a plan view of the switching circuitry associated with pixels of an organic light-emitting device;

figure 4 shows a cross-section of the circuitry of figure 3 on the line a-a';

figure 5 shows a circuit diagram of the device of figures 3 and 4;

figure 6 shows a schematic diagram of a multi-pixel device and its control apparatus; and

figures 7 and 8 show plots of current through a pixel against time.

Figures 3 to 5 show the TFT circuitry associated with a pixel 16 of an organic light-emitting display device. The light emitting material of the pixel is divided into four light-emitting areas 19a-d. The TFT circuitry comprises a common line 12 (which corresponds to the electrode 3 in figure 1) and a scan line 10 (which corresponds to the electrode 6) which are shared by the light-emitting areas 19a-d and other circuitry which is provided for each area individually. Each light-emitting area has a signal line 11a-d (which corresponds to the electrode 5 in figure 1), a switching transistor 13a-d (which corresponds to the transistor 7 in figure 1), a storage capacitor 14a-d (which corresponds to the capacitor 8 in figure 1) and a current transistor 15a-d (which corresponds to the transistor 9 in figure 1). Insulating regions of SiO₂, shown in figure 4, separate the component parts of the circuitry, and the circuitry is deposited on a glass substrate 17. At the outputs of transistors 15a-d are electrode pads 18a-d (see 18a in figure 4) of transparent indium-tin oxide ("ITO") to form the

anodes of the emitting device. The organic light-emitting material of the pixel is deposited on the pad in four separate sections (see 19a-d in figure 4) and a common cathode 20 (corresponding to electrode 2 in figure 1) is deposited on top of those sections. Light emission from the pixel towards a viewer is generally in the direction into the page in figure 3 and as shown by arrow B in figure 4.

The scan, signal and common lines are controlled by control units 31,34 (figure 5). In an alternative embodiment each light-emitting area 19a-d could be provided with an individual line equivalent to common line 12, each individual line being controlled independently by the control units.

The pixel of figures 3 to 5 forms part of a larger light-emitting device in which several thousand such pixels are arranged in orthogonal rows and columns. For instance, one typical size is 800 columns by 600 rows, giving a total of 480,000 pixels. The device could even be a colour display device having equal numbers of red, green and blue pixels. The additional units 50 in figure 5 are for controlling another pixel.

To manufacture the device the TFT circuitry is first deposited on to the glass substrate 17 in the normal way. Then the organic light-emitting material is deposited on the ITO pads 18d etc. The organic light-emitting material in this example is PPV. The PPV could be deposited as a layer over the entire device (e.g. by spin-coating a precursor polymer) and then patterned to form individual pixels or areas of pixels, or the pixels/areas could be deposited individually (e.g. by ink-jet printing), especially when forming a multi-colour (e.g. red/green/blue) device with pixels which each emit different colours of light. The resulting pixels are around 1000Å thick.

To deposit the light-emitting material by ink-jet printing the material is sprayed through an ink-jet printer spray head. A suitable spraying cycle is 14,400 drops per second, with a drop volume of 30pl

Figure 6 shows schematically an example implementation of a completed device 21 having numerous pixels 22, 23 etc. arranged in rows and columns, and each divided into a number of independently-controllable light-emitting areas as illustrated in figures 3 to 5. A control unit 24 is connected to the scan 25 and signal 26 lines and

capable of controlling the voltage of each. The control unit receives display signals at 27 (e.g. from a personal computer) and comprises a processing means 28 for decoding those signals into real-time brightness information for each pixel of the display. The processor 28 outputs address information on line 29 identifying each pixel in turn and brightness information on line 30 for that pixel. The brightness information could typically be a number from zero to, say, 16 or 64 giving the desired brightness of the pixel. The control unit includes an addressing switch unit 31 which receives address information at 32 identifying a pixel and brightness information at 33, addresses the identified pixel by selecting the scan and signal lines at whose intersection the pixel lies and applies appropriate voltages to the scan and signal lines each to store on that pixel's storage capacitor the charge required to turn the pixel on to the desired brightness as indicated on line 33. The control unit or any part of it could be formed on the backplane of the display itself.

In the prior art device of figure 1, in which pixels are driven in the transition region of their current transistors, the addressing switch 31 could receive addressing and brightness information directly from the processor 28. In the present exemplary embodiment of the present invention there is however an intermediate processor 34 between the processor 28 and the switch unit 31. The intermediate processor receives the brightness information for each pixel from processor 28 by lines 29 and 30 and stores the desired brightness of each pixel in memory 35. The intermediate processor then controls the addressing switch unit to fix the brightness of the pixels in one or both of two ways.

The first way of fixing the brightness is by turning the pixels on and off rapidly with a duty cycle that achieves the desired brightness from each pixel when averaged over time (for the duration of a cycle). For instance, if half-brightness is desired (e.g. a brightness of 32 in the 64 grey-scale scheme mentioned above) the pixel is switched so as to be fully on for half of the time and fully off for half of the time. An impression of flickering can be avoided by switching the pixel rapidly between its on and off states. A suitable switching frequency to give a viewer an impression of constant light output from the device is 30 to 50Hz or more. The brightness values output by the intermediate processor 34 on line 33 indicate either fully on or fully off; intermediate values need not be used. Therefore, the current transistors of the

pixels of the device are always (apart from transiently during turn-on and turn-off) operating in the predictable, flat on and off regions shown in figure 2, and the brightnesses of the pixels can more easily be made consistent.

A number of detailed drive schemes could be used to achieve the desired brightness of each pixel. For instance, the pixel could (except when it is desired to be at full or zero brightness) be turned on once and off once in each cycle, with the time between the on and off switching chosen to achieve the required duty cycle (see figure 7), or more than once (see figure 8). Figure 8 shows a plot of applied current against time for a single pixel. The lines 36 separate cycles of the drive scheme. During the three cycles shown in figure 8 the brightness of the pixel is increased from around 10% to around 40%. The on-time of the pixel is applied as a series of pulses of equal length t_p which, when added together, give the total on-time per cycle needed to achieve the required duty cycle. With the total on-time per cycle kept the same, the pattern of current between the on time and the off time can be varied to suit other requirements - e.g. to reduce flicker or cross-talk.

The second way to fix the brightness is to make use of the fact that the separate light-emitting areas of each pixel can be individually controlled. The brightness of the pixel can be controlled by switching on one or more of the light-emitting areas. As before, the brightness is controlled by control unit 24. The addressing switch 31 again receives address information on line 32 identifying each pixel in turn and brightness information on line 33 for that pixel. This time the brightness information has a value from 0 to 16, indicating the desired brightness. The addressing switch 31 includes a multi-area processor 45 which receives the address information for, say, the pixel shown in figures 3 to 5, identifies the scan and signal lines needed to address the individual light-emitting areas of that pixel and based on the brightness value received at 33 applies the appropriate voltages to those lines to turn none, one, two three or all of the light-emitting areas of the pixel fully on so as to achieve the desired brightness. Those selected areas can be left fully on until the brightness of the pixel is to be changed.

The light-emitting areas into which each pixel is divided could be of the same size or of different sizes. One useful division is, where the pixel is to be divided into n light-

emitting areas, for the sizes of those areas to be $1/(2^n-1)$, $2/(2^n-1)$, $4/(2^n-1)$, ..., $(2^{(n-1)})/(2^n-1)$ of the full size of the pixel. This arrangement provides 2^n grey-scale values from those n pixels. For example, in figure 3 the area of light-emitting area 19d is twice that of area 19c, which is twice that of area 19b, which is twice that of area 19a.

The light-emitting areas could be defined as parallel strips running across the pixel, as in figure 3, or in other ways.

To achieve even more grey-scales the two ways of controlling brightness described above could be combined, so that a device having sub-divided pixels of the type shown in figures 3 to 5 is driven by a pulsed drive scheme as described with reference to figure 7 or 8. Either or both of those two ways of controlling brightness could also be combined with voltage control, as described with reference to figure 1, where the voltage applied to each light-emitting area is also capable of being varied to a level that is not fully on or off, to give even more available grey levels. For example, in a device with each pixel divided into four areas as shown in figure 3, driven with one of sixteen available drive schemes as shown in figure 7, where the peak voltage is chosen from one of 16 levels, 4096 grey levels are available.

A pulsed drive scheme could be applied to a device having unitary pixels, rather than the sub-divided pixels of the device of figures 3 to 5.

The present invention may include any feature or combination of features disclosed herein either implicitly or explicitly or any generalisation thereof irrespective of whether it relates to the presently claimed invention. In view of the foregoing description it will be evident to a person skilled in the art that various modifications may be made within the scope of the invention.

CLAIMS

1. An organic light-emitting device having:
an organic light-emitting region comprising a plurality of organic light-emitting pixels;
switch means each associated with a respective pixel for switching power to that pixel; and
drive means for driving each switch means to cycle between a first, low power mode and a second, high power mode, at a frequency sufficient to cause light emission from the associated pixel to appear substantially continuous, the duration of the high power mode relative to the low power mode being variable so as to vary the average brightness of the pixel.
2. An organic light-emitting device as claimed in claim 1, wherein in the first mode the pixel is substantially non-emissive.
3. An organic light-emitting device as claimed in claim 1 or 2, wherein the switch means comprises a transistor switch for switching the power supplied to the pixel.
4. An organic light-emitting device as claimed in claim 3, wherein in the second mode the transistor is in its fully on state.
5. An organic light-emitting device as claimed in claim 3, wherein in the second mode the transistor is in its fully off state.
6. An organic light-emitting device as claimed in any of claims 3 to 5, wherein the drive means is capable of driving the switch means to a non-instantaneous partially on state.
7. An organic light-emitting device as claimed in any preceding claim, wherein the switch means comprises a thin-film transistor switch.

8. An organic light-emitting device as claimed in any preceding claim, wherein the drive means drives each switch means to cycle between the first mode and the second mode at a frequency of at least 30Hz.
9. An organic light-emitting device as claimed in any preceding claim, wherein each pixel comprises at least two independent light-emitting areas, and comprising switch means each associated with a respective one of the light-emitting areas for independently switching power to that light-emitting area under the control of the drive means.
10. An organic light-emitting device as claimed in claim 9, wherein the light-emitting areas of each pixel are of different areas.
11. An organic light-emitting device as claimed in any preceding claim, wherein the organic light-emitting pixels are formed of a light-emitting polymer material.
12. An organic light-emitting device as claimed in any preceding claim, wherein the organic light-emitting pixels are formed of a light-emitting conjugated material.
13. An organic light-emitting device as claimed in any preceding claim, wherein the organic light-emitting pixels are formed of poly(p-phenylenevinylene) or a derivative thereof.
14. An organic light-emitting device as claimed in any preceding claim, wherein each pixel is deposited by ink-jet printing.
15. An organic light-emitting device comprising:
 - an organic light-emitting region comprising a plurality of organic light-emitting pixels, each pixel comprising at least two independent light-emitting areas;
 - a switch arrangement associated with each pixel and comprising switch means each associated with a respective light-emitting area of that pixel for switching power to that area; and
 - a control means for addressing each pixel by its associated switch arrangement and controlling the brightness of each pixel by selectively driving one or

more of the corresponding switch means to cause selected ones of the light-emitting areas of that pixel to emit light.

16. An organic light-emitting device as claimed in claim 15, wherein the light-emitting areas of each pixel are of different sizes.

17. An organic light-emitting device as claimed in claim 16, wherein the light-emitting areas of each pixel are of different areas.

18. An organic light-emitting device as claimed in any of claims 15 to 17, wherein the organic light-emitting areas are formed of a light-emitting polymer material.

19. An organic light-emitting device as claimed in any of claims 15 to 18, wherein the organic light-emitting areas are formed of a light-emitting conjugated material.

20. An organic light-emitting device as claimed in any of claims 15 to 19, wherein the organic light-emitting areas are formed of poly(p-phenylenevinylene) or a derivative thereof.

21. An organic light-emitting device as claimed in any of claims 15 to 20, wherein each organic light-emitting area is deposited by ink-jet printing.

22. An electroluminescent device substantially as herein described with reference to figures 3 to 8 of the accompanying drawings.

1 / 4

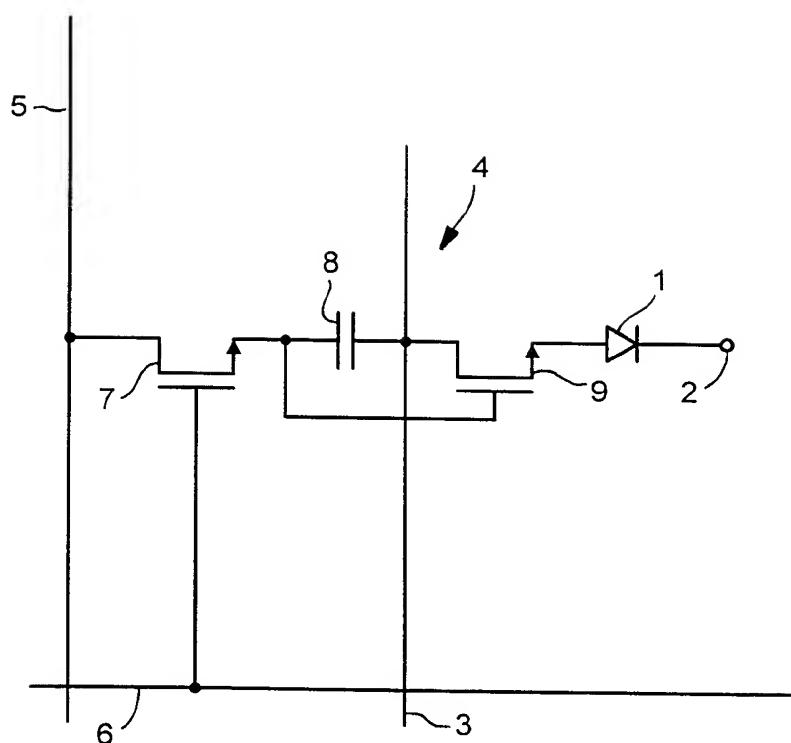


FIG. 1

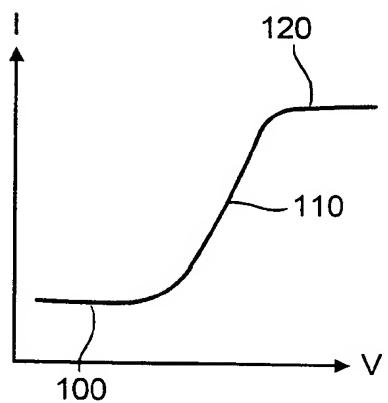


FIG. 2

2 / 4

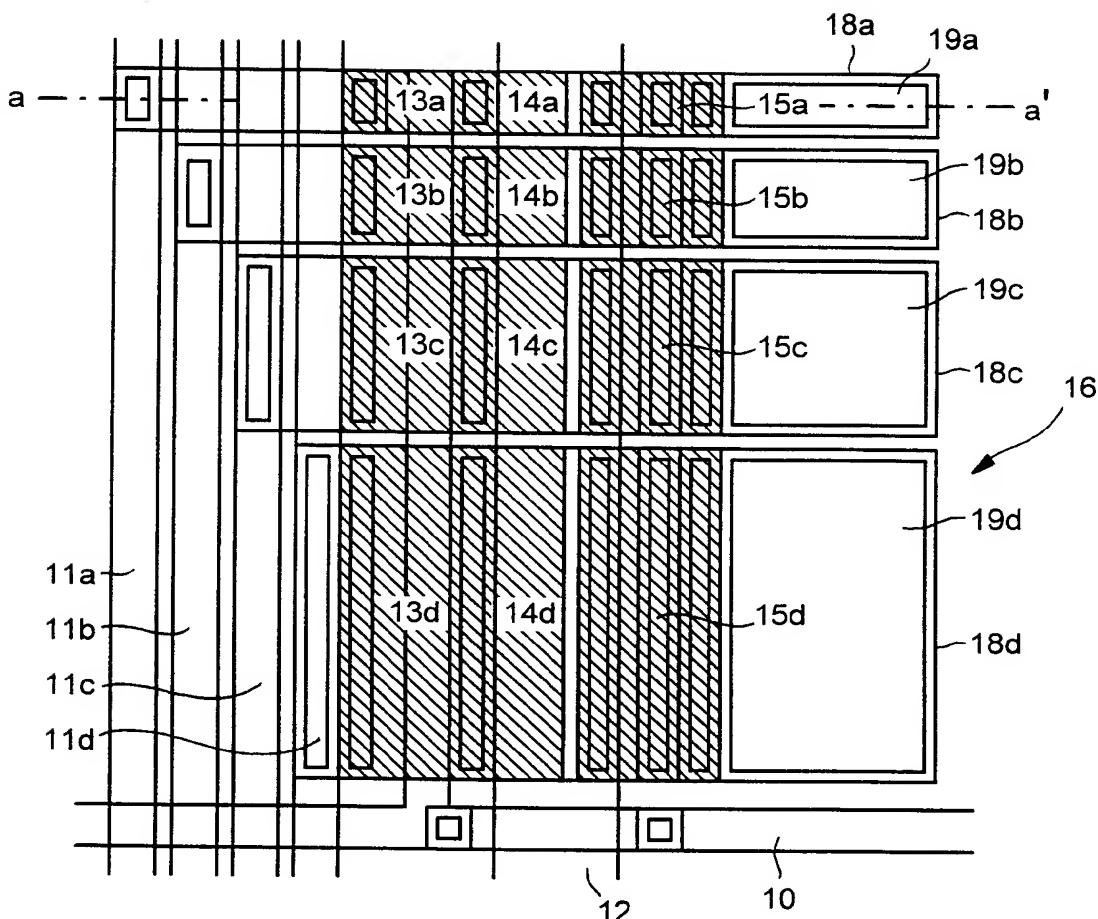


FIG. 3

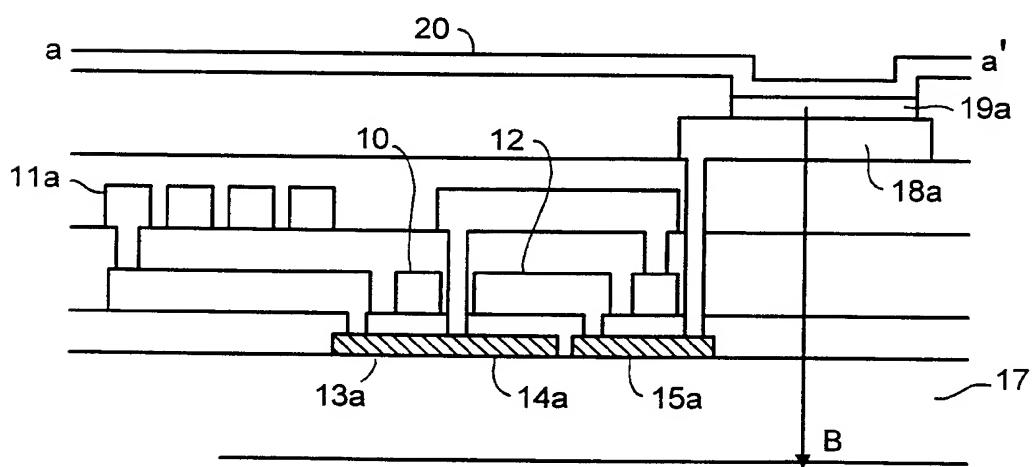


FIG. 4

3 / 4

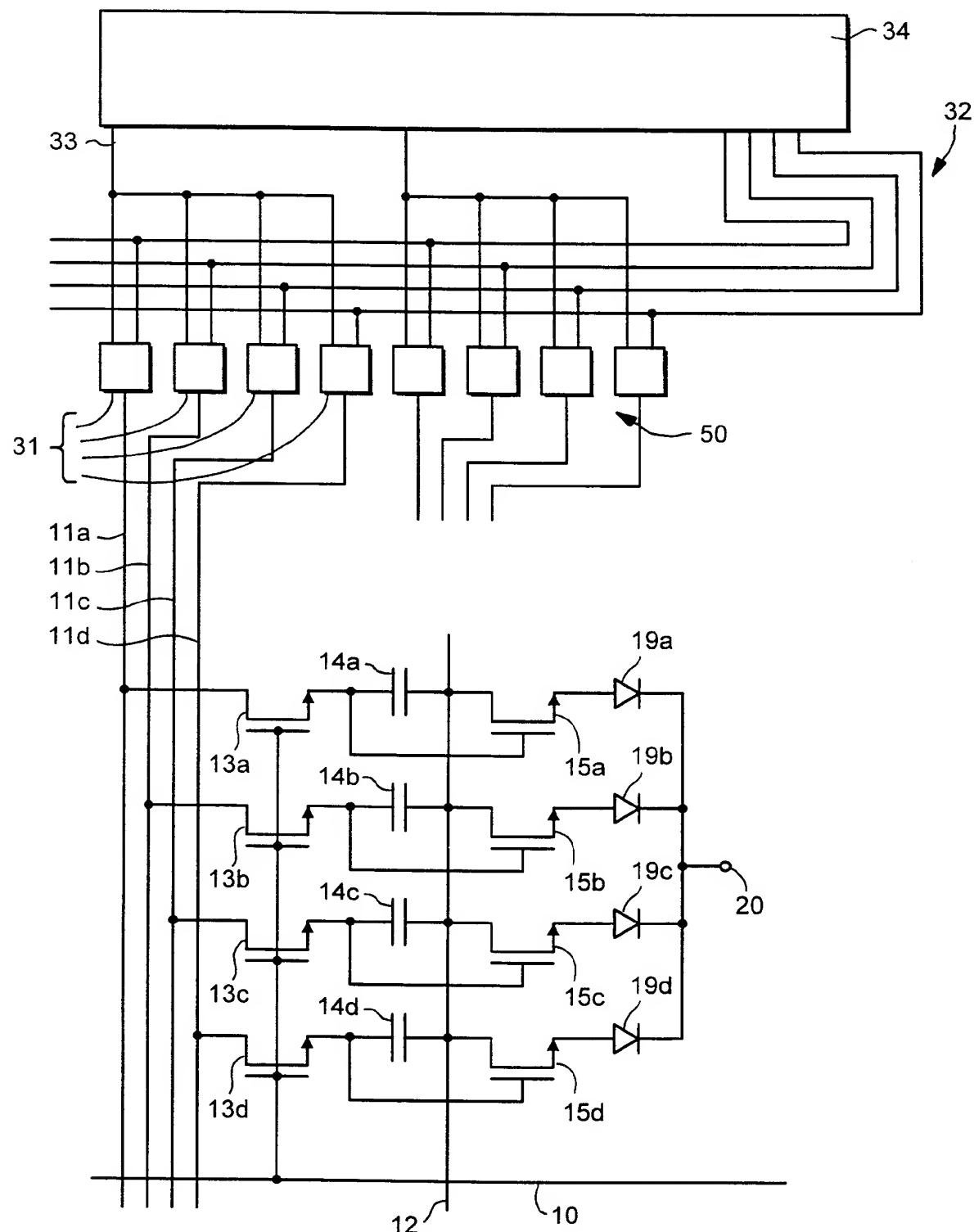


FIG. 5

4 / 4

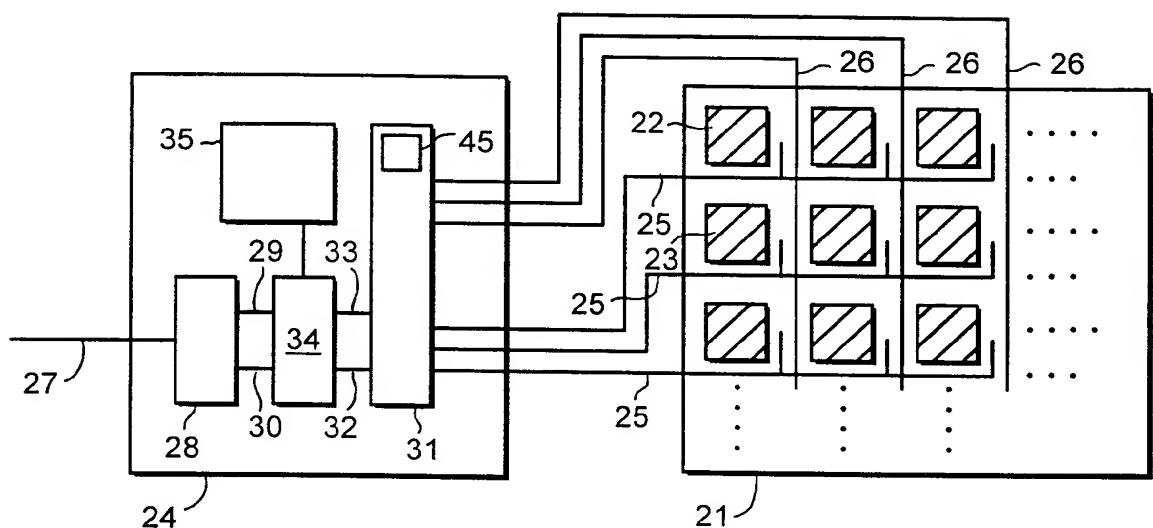


FIG. 6

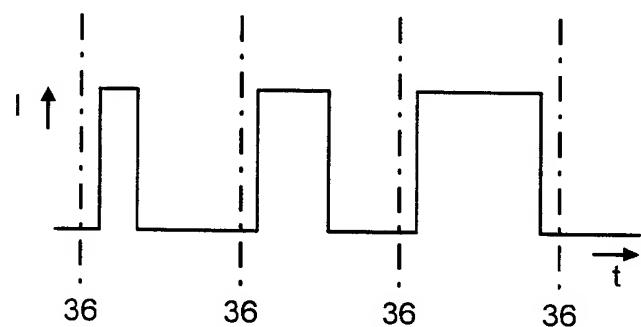


FIG. 7

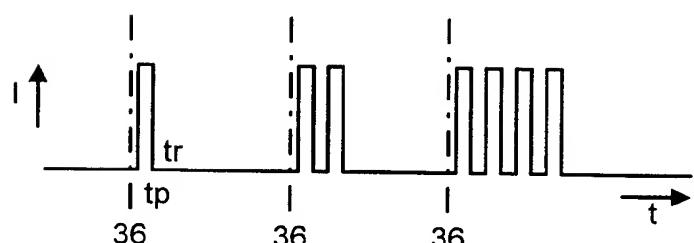


FIG. 8

INTERNATIONAL SEARCH REPORT

International Application No

PCT/GB 99/00383

A. CLASSIFICATION OF SUBJECT MATTER

IPC 6 G09G3/32

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 G09G

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
E	WO 99 12150 A (KIMURA MUTSUMI ;SEIKO EPSON CORPRORATION (JP)) 11 March 1999 see abstract; figures ----	15-17,22
A	US 5 714 968 A (IKEDA NAOYASU) 3 February 1998 see column 4, line 42 - column 6, line 27; figures ----	1,3,7
A	PATENT ABSTRACTS OF JAPAN vol. 095, no. 008, 29 September 1995 -& JP 07 122360 A (MITSUBISHI CHEM CORP), 12 May 1995 see abstract ----	1,3,7
A	EP 0 762 374 A (MOTOROLA INC) 12 March 1997 see abstract; figures ----	1,3,7
	-/-	

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

° Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority, claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

28 May 1999

Date of mailing of the international search report

07/06/1999

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

De Laere, A

INTERNATIONAL SEARCH REPORT

Int'l Application No
PCT/GB 99/00383

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 4 074 318 A (KAPES JR WILLIAM JOHN) 14 February 1978 see column 2, line 27-40 see column 4, line 34 - column 5, line 8 -----	1,15

INTERNATIONAL SEARCH REPORT

Information on patent family members					International Application No PCT/GB 99/00383
Patent document cited in search report	Publication date	Patent family member(s)		Publication date	
WO 9912150	A	11-03-1999	JP	11073158 A	16-03-1999
US 5714968		A	03-02-1998	JP	2689916 B
			JP	8054835 A	27-02-1996
			JP	2689917 B	10-12-1997
			JP	8054836 A	27-02-1996
EP 0762374		A	12-03-1997	US	5748160 A
			JP	9138659 A	05-05-1998
			JP	9138659 A	27-05-1997
US 4074318		A	14-02-1978	AU	3139877 A
			BE	861596 A	14-06-1979
			DE	2754264 A	31-03-1978
			FR	2373929 A	15-06-1978
			GB	1597228 A	07-07-1978
			JP	53074829 A	03-09-1981
			NL	7713545 A	03-07-1978
			SE	7713582 A	15-06-1978
					14-06-1978